

Fig. 1A

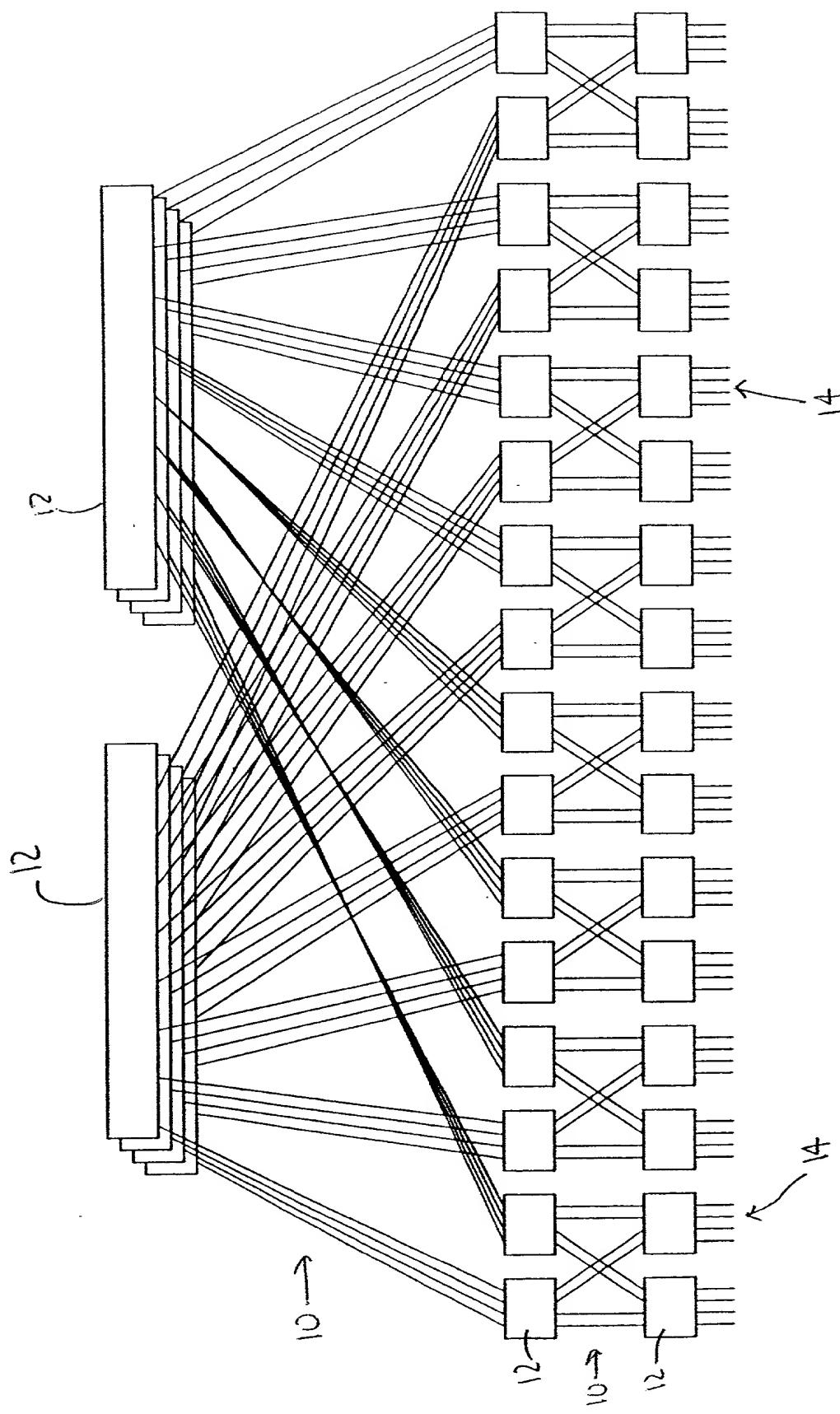


FIG. 1B

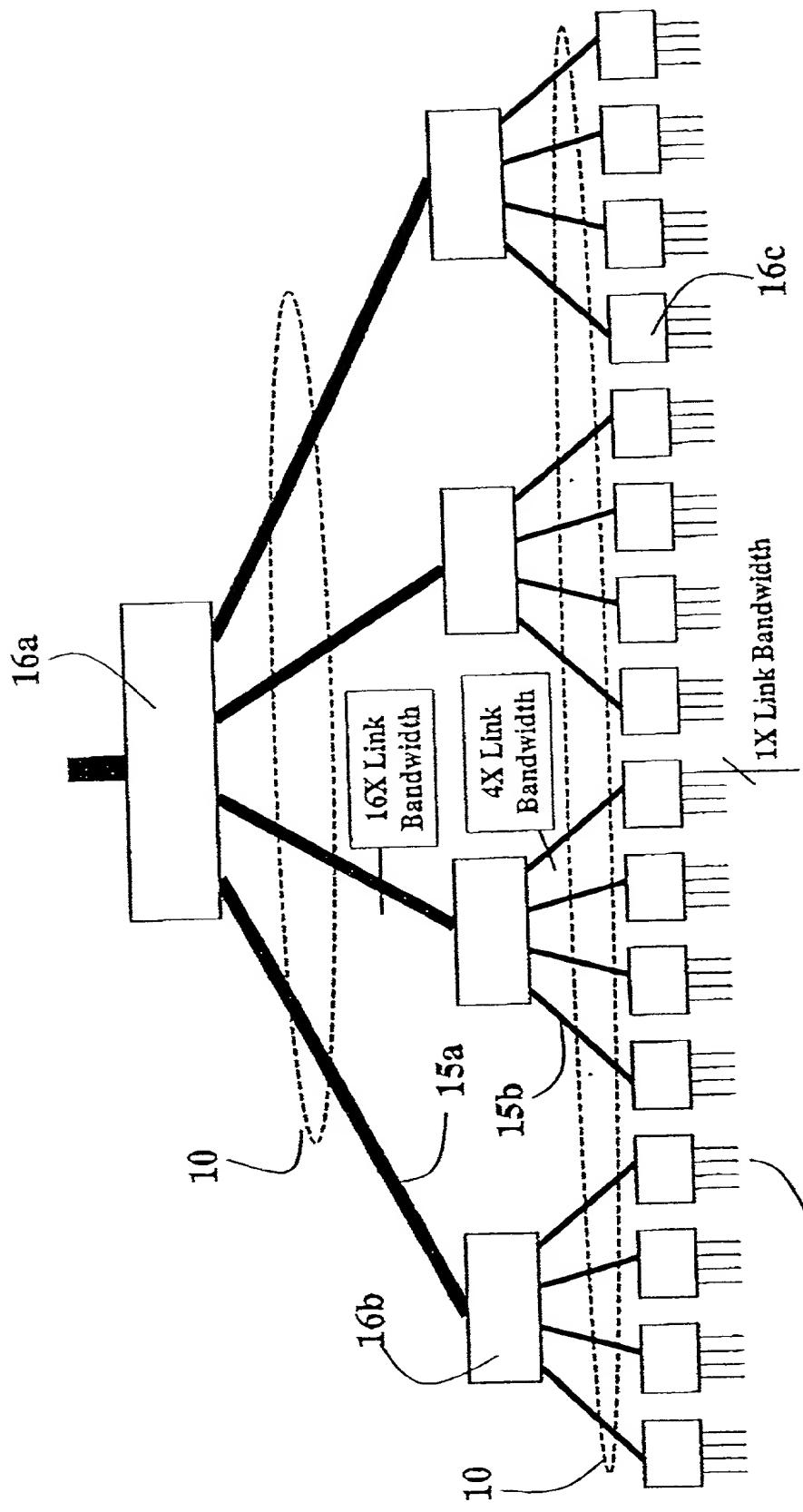


Fig. 1C

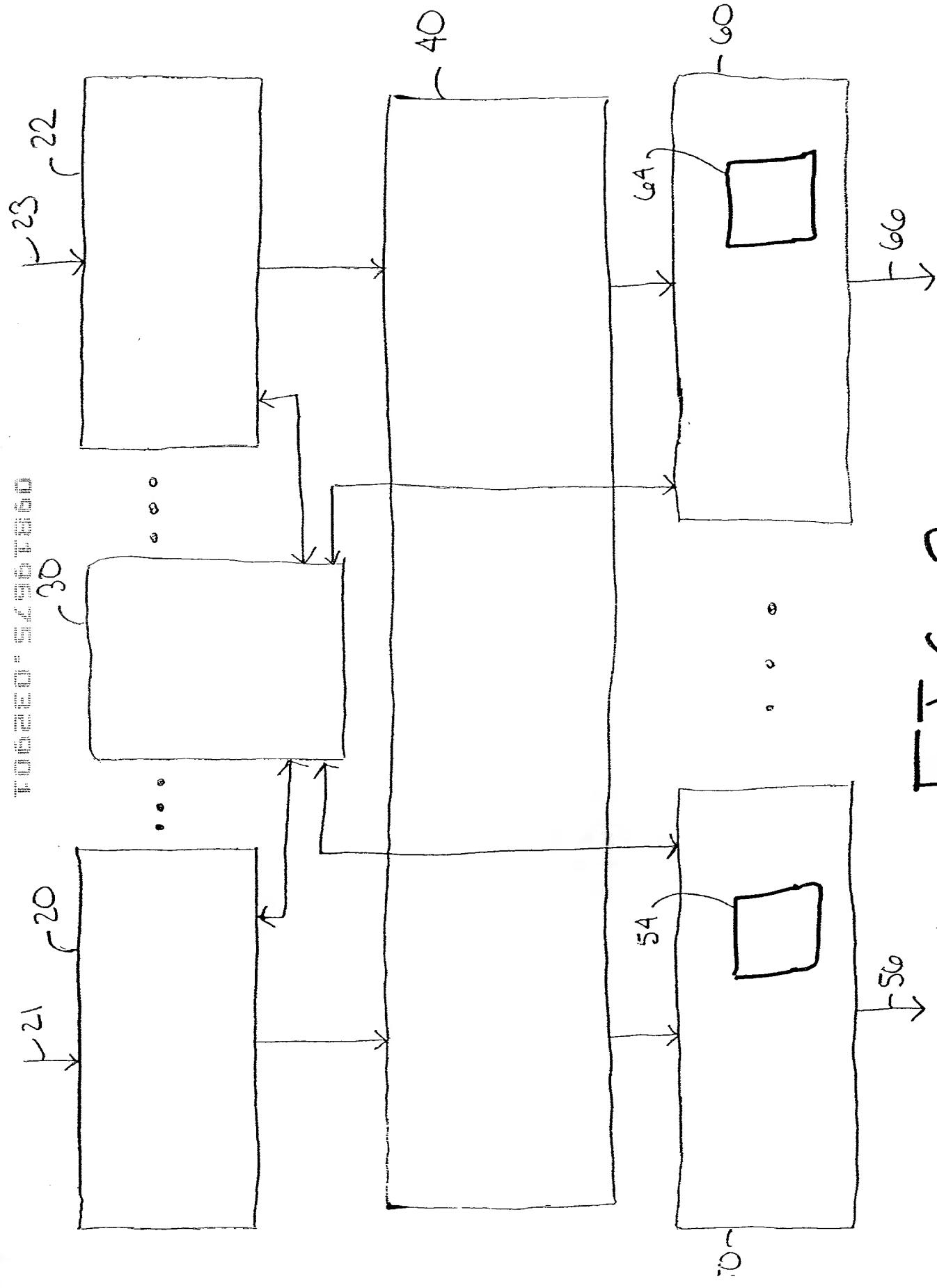


FIG. 2

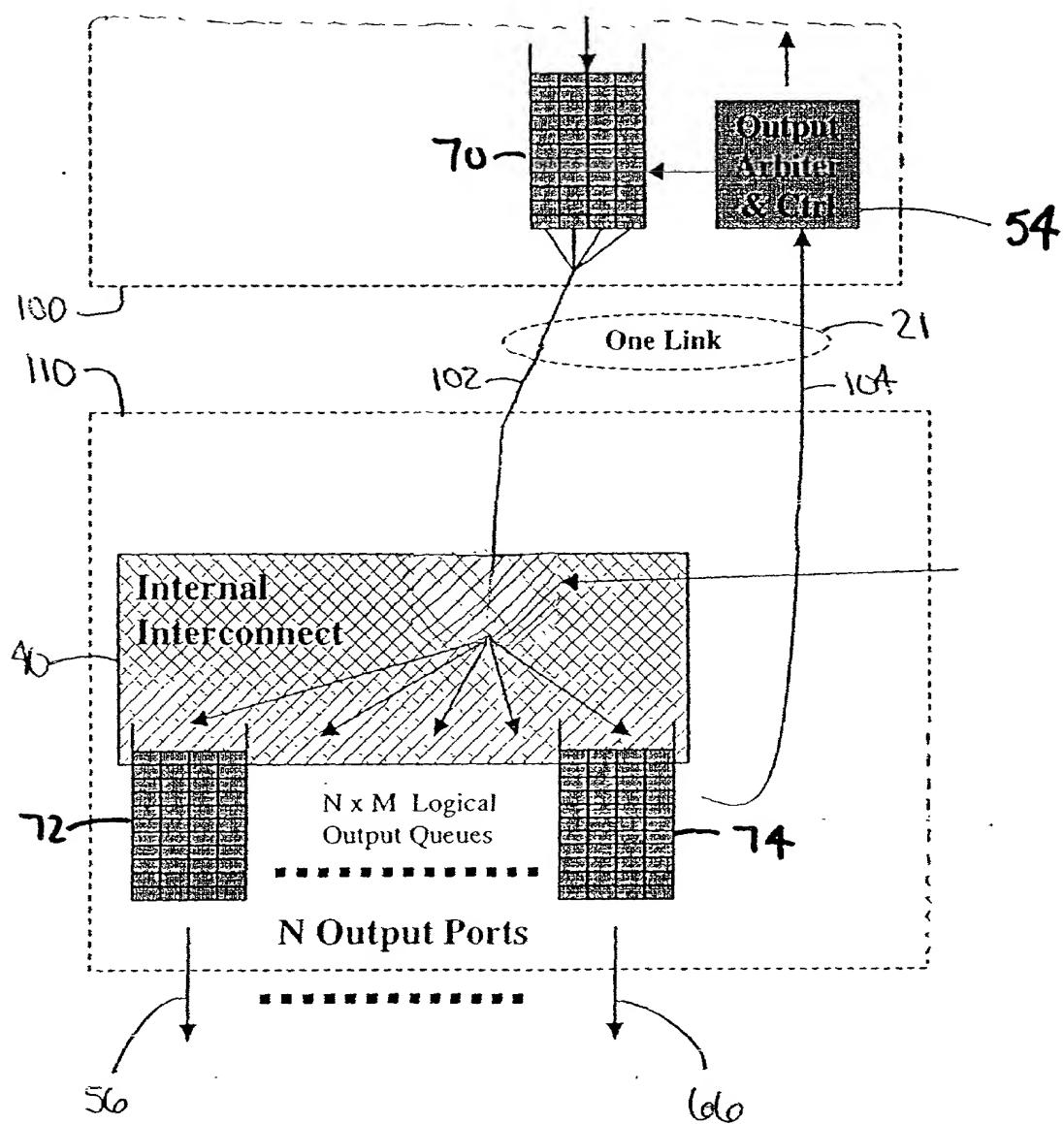


FIG. 3

FIG.4A

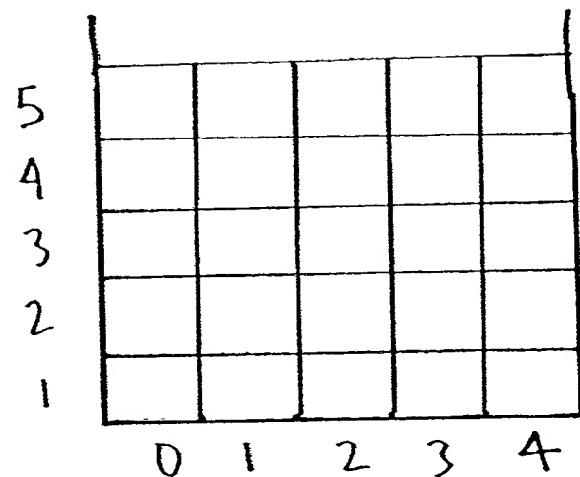


FIG.4B

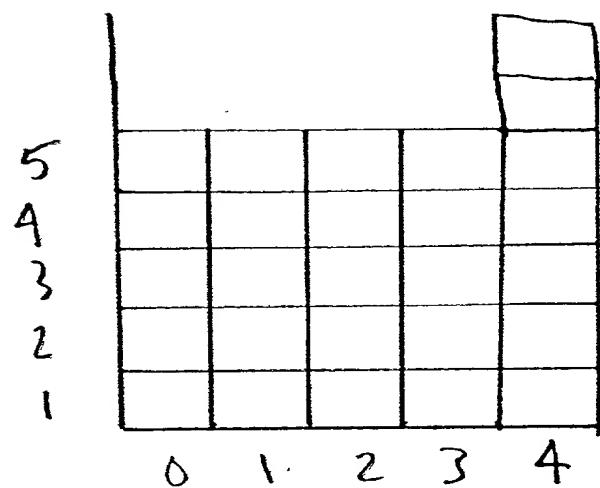
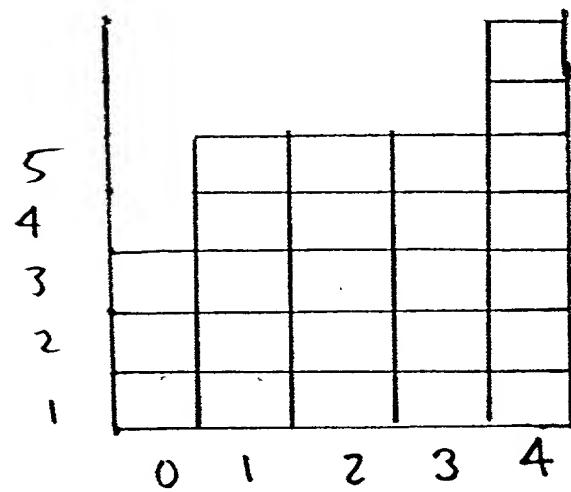


FIG.4C



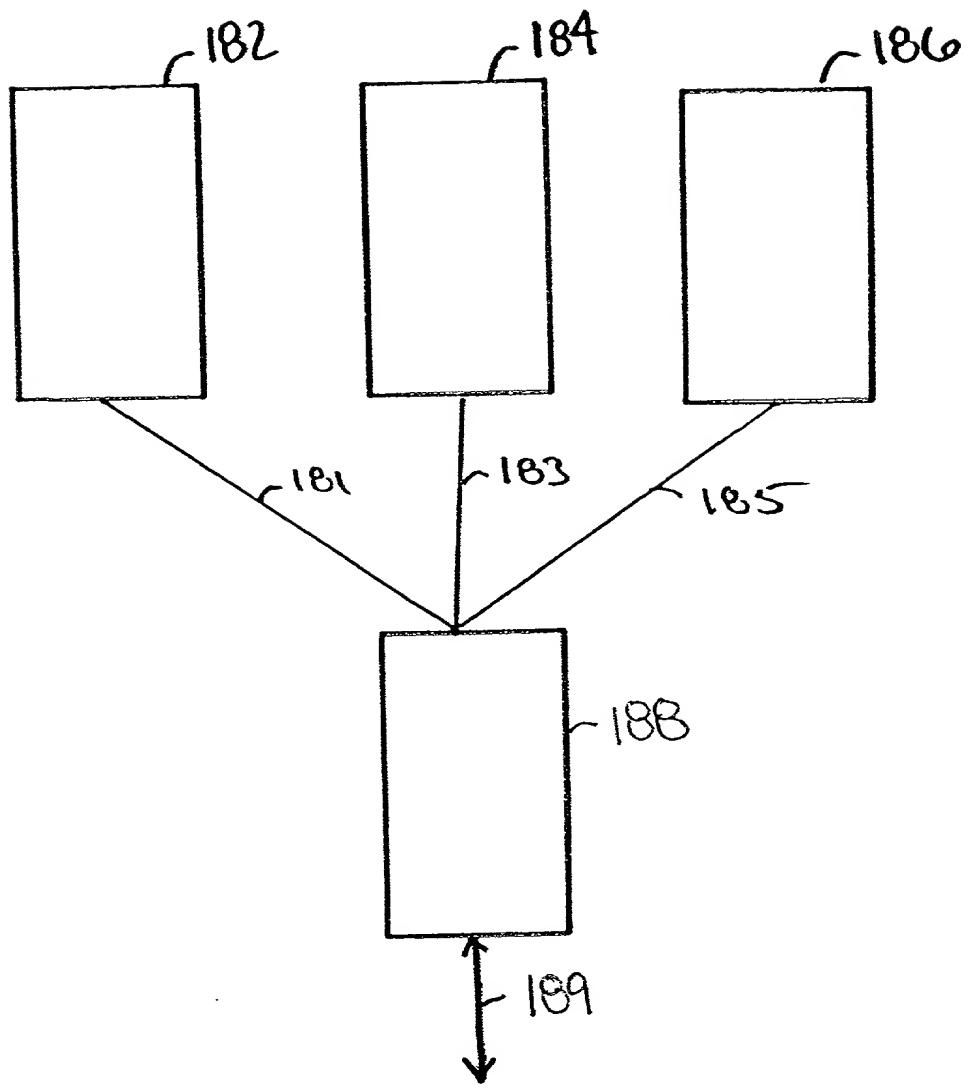


FIG. 5

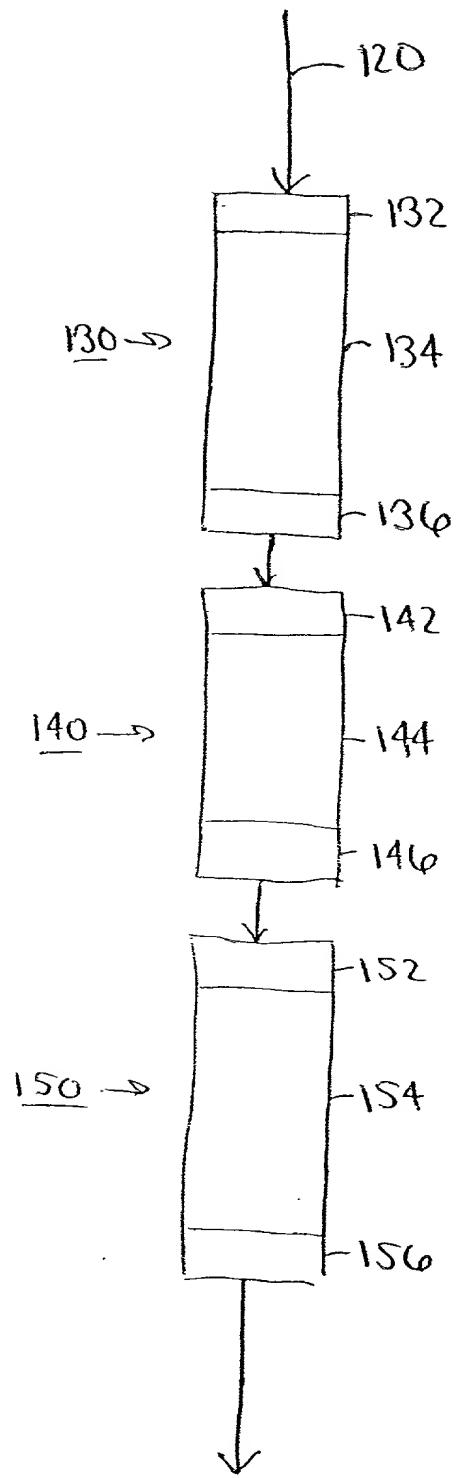
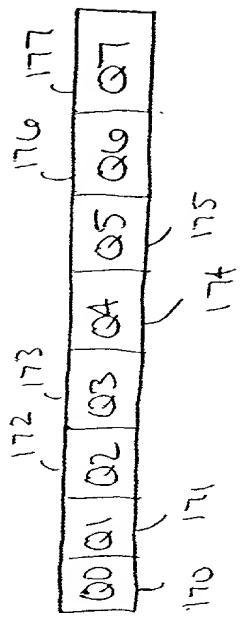


FIG. 6

31 →



E G H

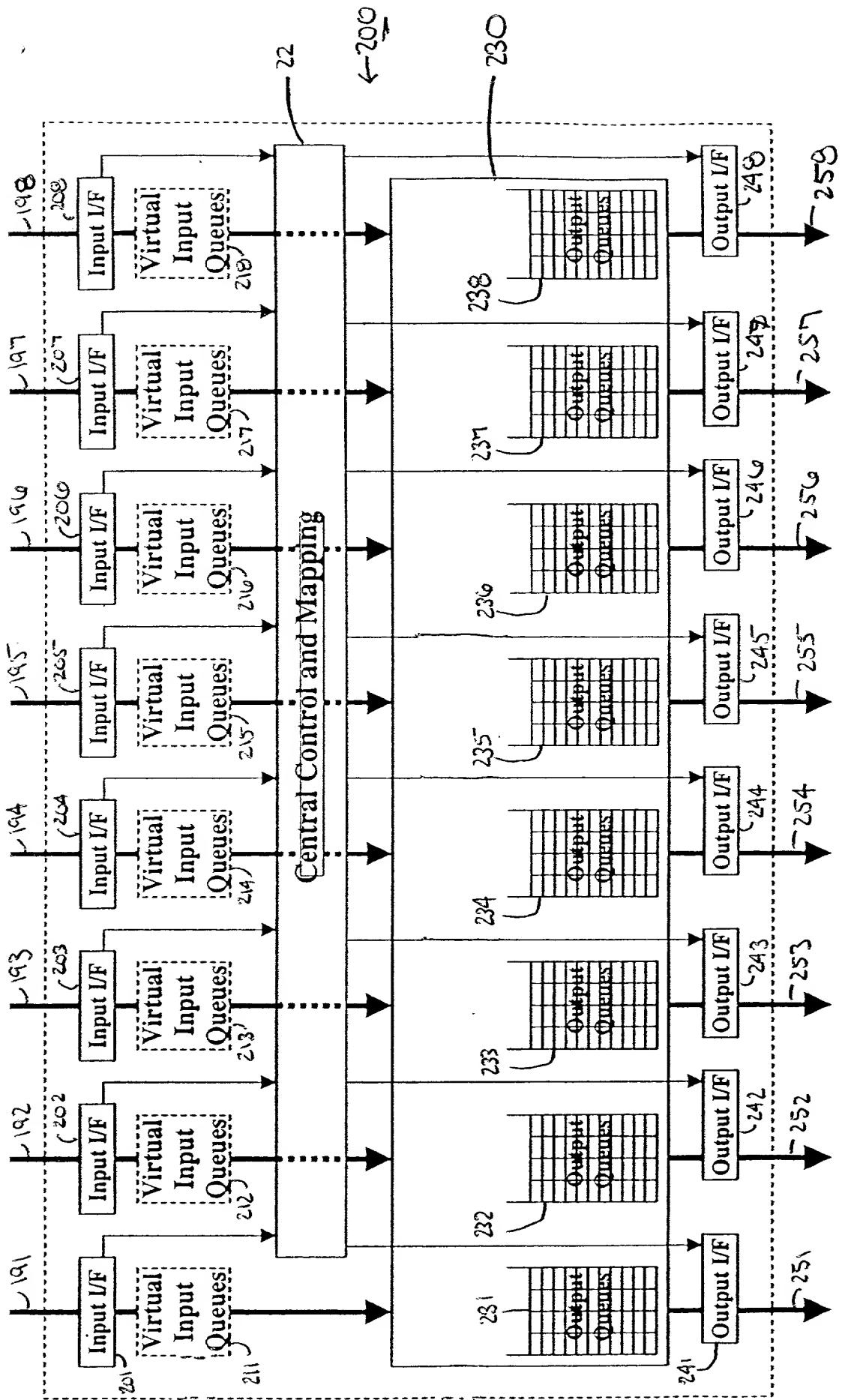


FIG. 8

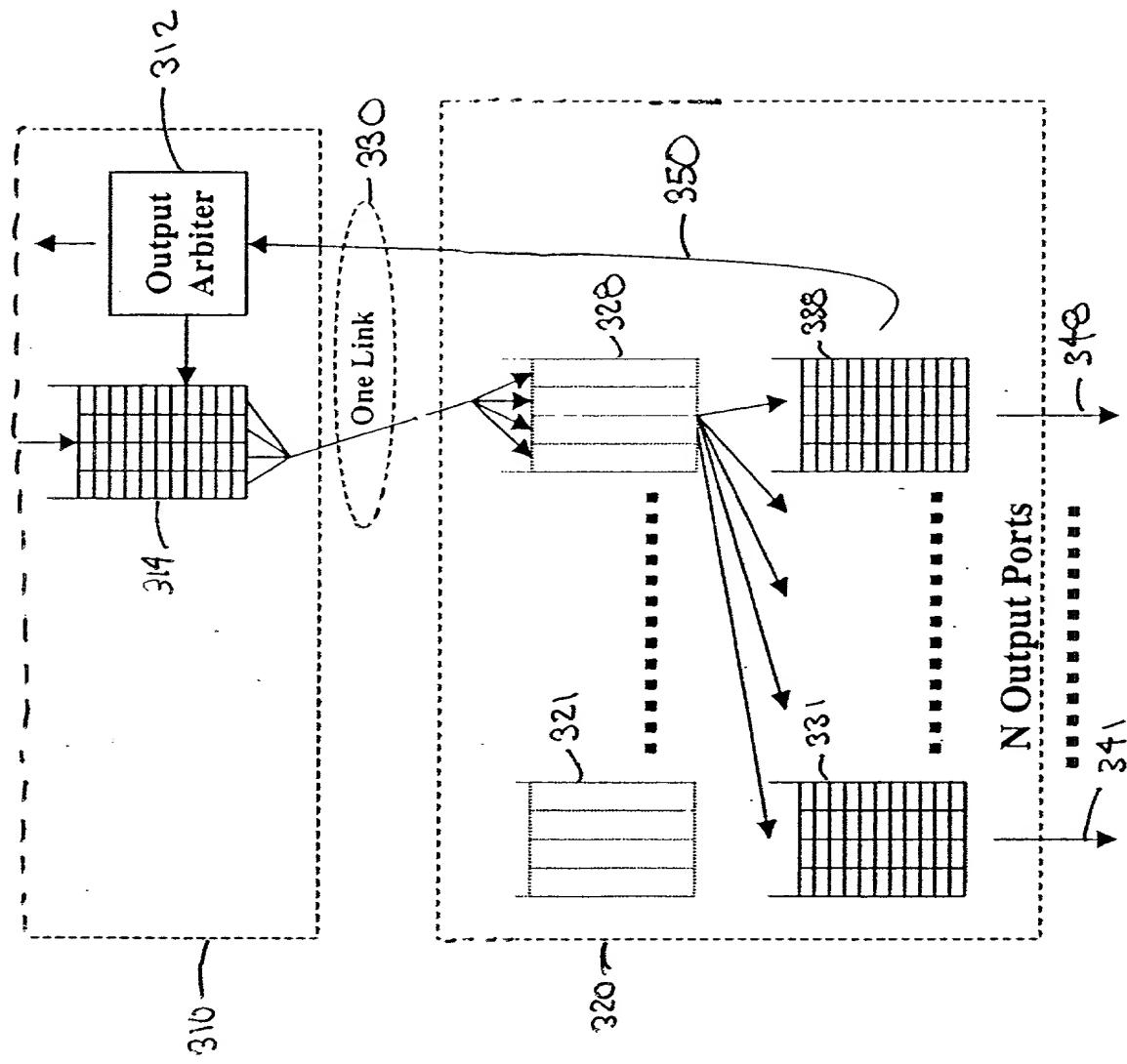
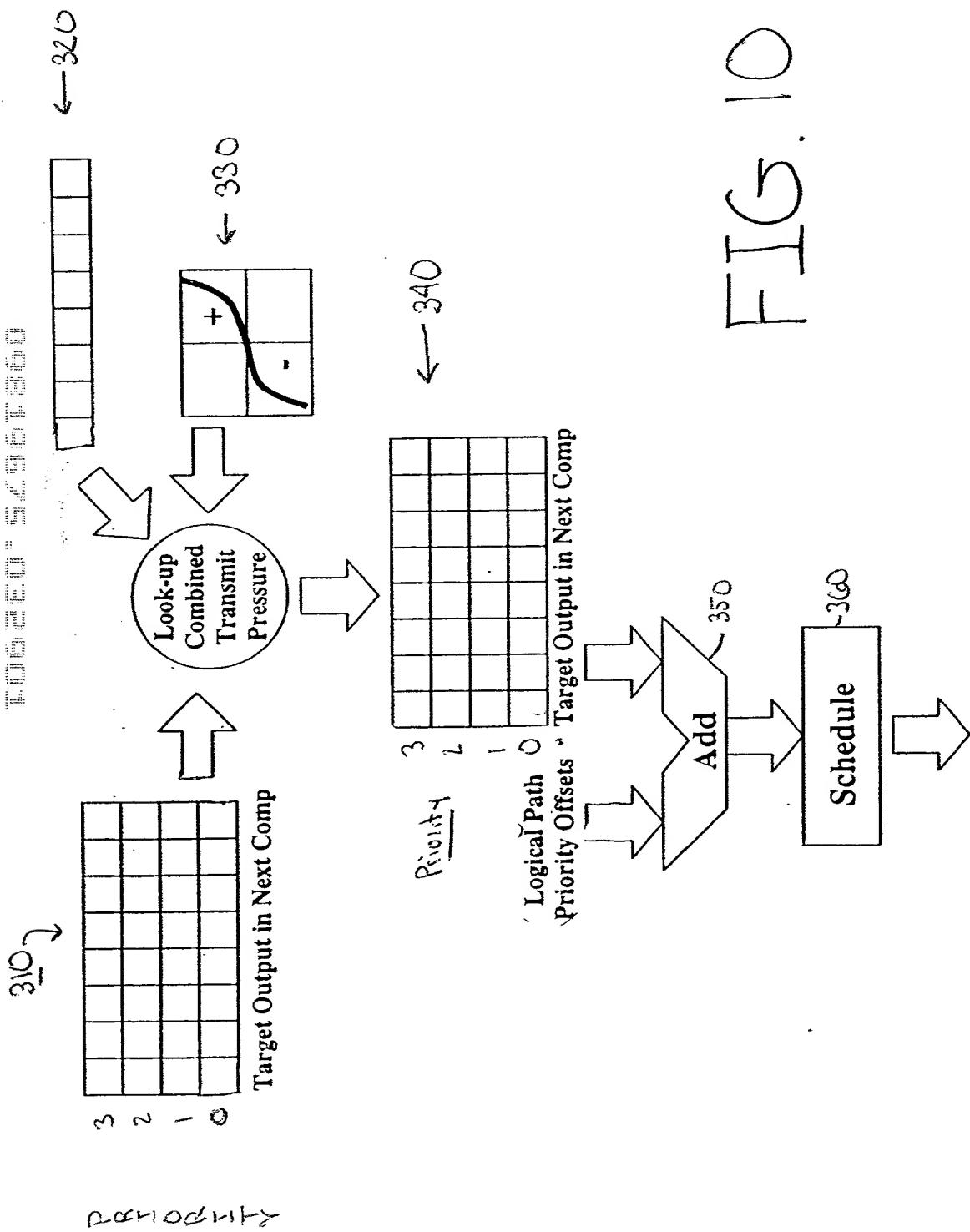


FIG. 9



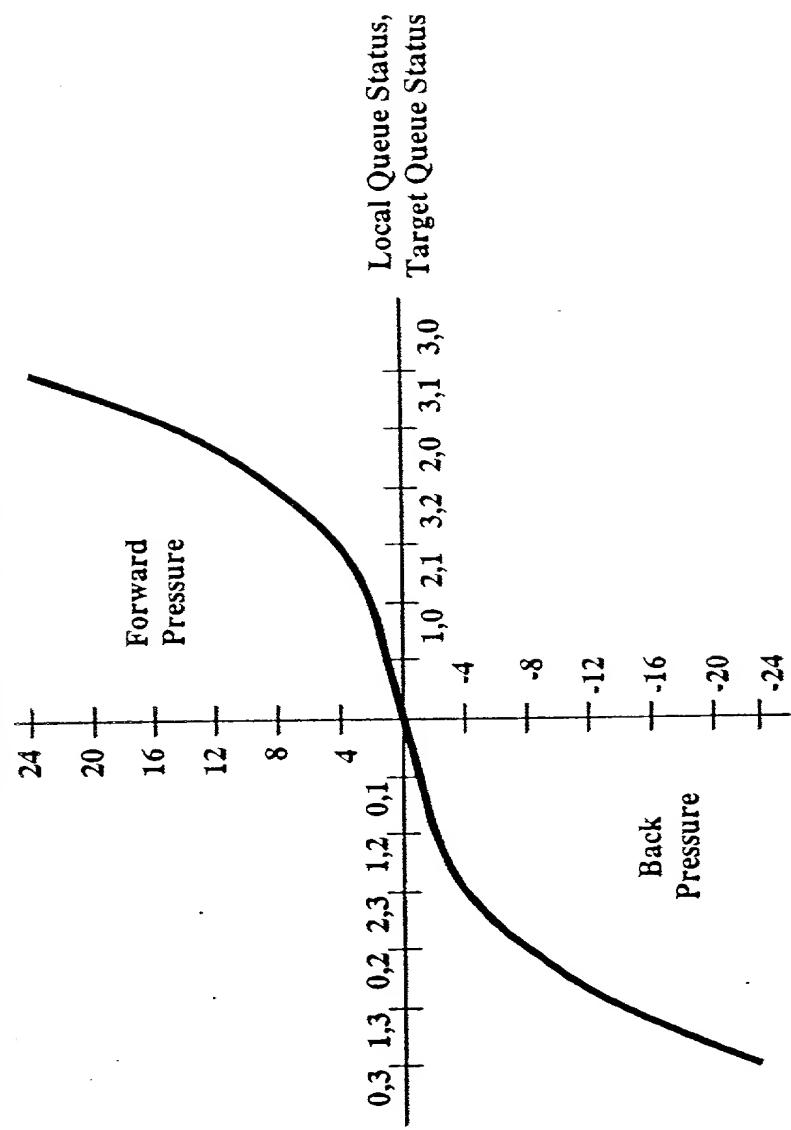


FIG. 11

| Status of Target Queue in Next Comp | |
|-------------------------------------|-----|
| | 0 |
| 0 | 0 |
| 1 | 1 |
| 2 | 8 |
| 3 | 24 |
| | 1 |
| | 2 |
| | 3 |
| | -1 |
| | -8 |
| | -24 |
| | 0 |
| | -2 |
| | 0 |
| | -4 |
| | 0 |
| | 4 |
| | 0 |

Logical Path Priority Vector

| Logical Path | 0 | 1 | 2 | 3 |
|-----------------|----|---|---|---|
| Priority Offset | 15 | 8 | 3 | 0 |

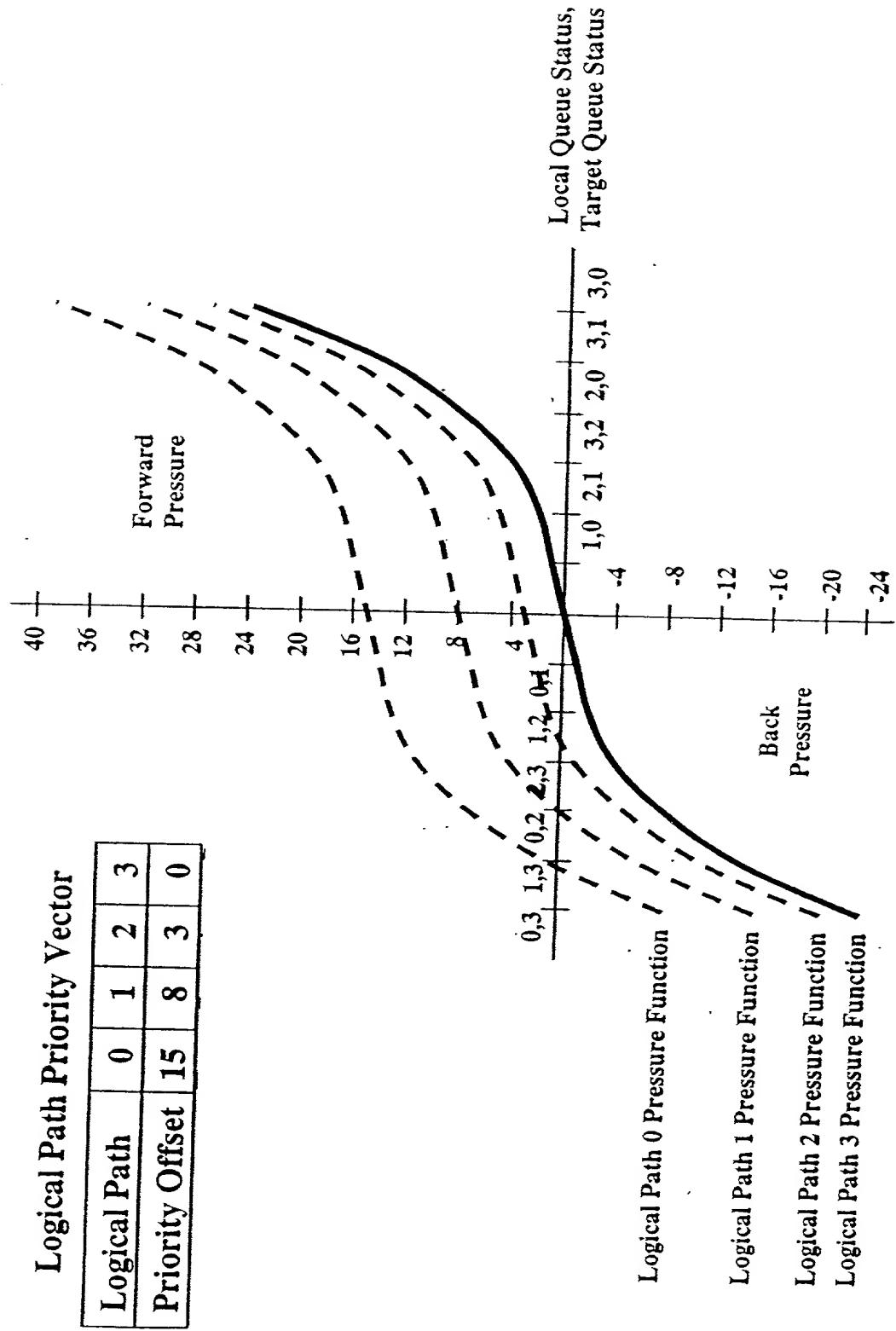


FIG. 12